

# Microcontroller Prog. Basics

## Department of Instrumentation Engineering

### | Lecture 2 |

# Numbering and Coding Systems

# A. Introduction

- ◆ *Digital systems* are built from circuits that process **binary digits** - **0**s and **1**s, yet many real-life problems are based on numbers. Therefore we have to establish a correspondence between binary digits processed by digital circuits and real-life numbers, events and conditions.
- ◆ Since the binary number system is universally employed in digital computer, it is useful and necessary to understand the general properties of number systems and the methods of conversion from one to another.

# A1. Basic Terms

**Number System** : a *code system* representing *quantity*

**Base (Radix)** : the base or radix of a number system refers to the *number of basic symbols* used.

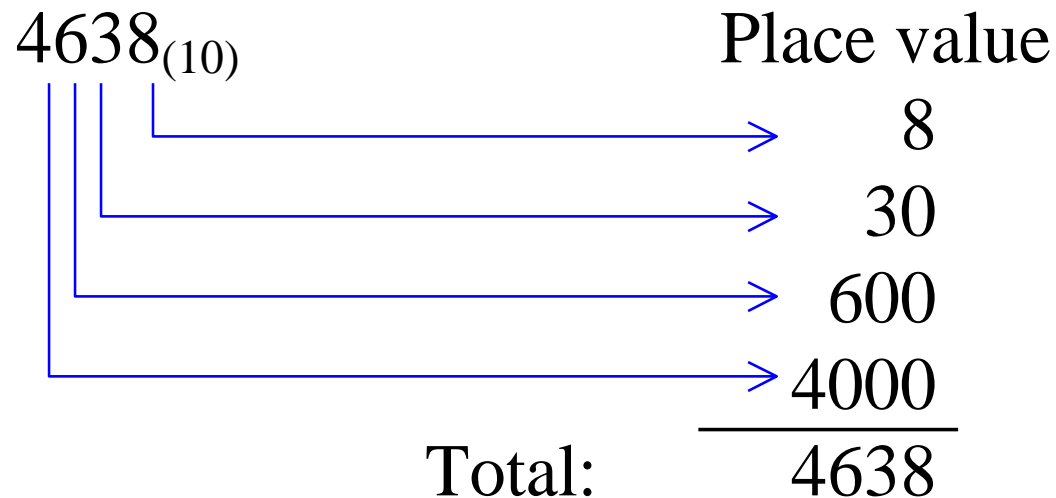
**Digit** : a *single basic symbol* used in a number system

**Bit** : bit is the abbreviated form of *binary digit*

**Byte** : an *entity* formed by combining *8 bits*

## A2. Position Notation

Consider the decimal number  $4638$ :



Each *digit* carries different *place value* that affects the *total value* of a number.

## B. Definition of a Number System

For a given number system, a quantity **N** with base **R** can be expressed as :

$$N_{(R)} = d_n R^n + d_{n-1} R^{n-1} + \dots + d_2 R^2 + d_1 R^1 + d_0 R^0$$

where  $d_n$  is the digit of the corresponding position

### **Example: Decimal System**

**base = 10**; ie. 10 different digits or symbols

(0, 1, 2, 3, 4, 5, 6, 7, 8, 9)

## B. Definition of a Number System

Digits in different positions of a decimal number represent different value

**place value**

$$\begin{array}{ccccccc}
 & & \downarrow & & \downarrow & & \downarrow & & \downarrow & & \\
 & & \text{---} & & \text{---} & & \text{---} & & \text{---} & & \\
 6236_{(10)} & = & 6000 & + & 200 & + & 30 & + & 6 & & \\
 & = & 6 \times 10^3 & + & 2 \times 10^2 & + & 3 \times 10^1 & + & 6 \times 10^0 & & \\
 & & \uparrow & & \uparrow & & \uparrow & & \uparrow & & \\
 & & \text{---} & & \text{---} & & \text{---} & & \text{---} & & \\
 & & \text{column weights} & & & & & & & & 
 \end{array}$$

# B1. Binary Number System

**base = 2**

2 symbols : **0** and **1**

$$\mathbf{N}_{(2)} = d_n 2^n + d_{n-1} 2^{n-1} + d_{n-2} 2^{n-2} + \dots + d_2 2^2 + d_1 2^1 + d_0 2^0$$

**Example 1:** Determine the value of the binary number 11001.

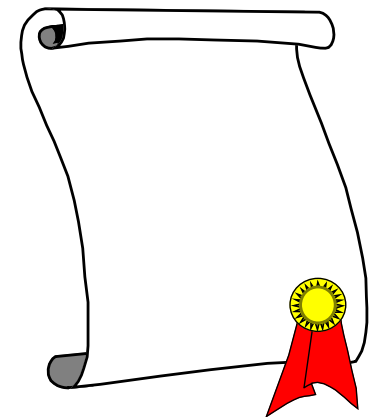
$$\begin{aligned} 11001_{(2)} &= 1 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 0 \times 2^1 + 1 \times 2^0 \\ &= 16 + 8 + 0 + 0 + 1 \\ &= \mathbf{25}_{(10)} \quad \dots \text{converted to decimal equivalent} \end{aligned}$$

# B1. Binary Number System

## Classwork 1

Convert the following binary numbers into decimal:

(a)  $1001100_{(2)}$  ; (b)  $1101011_{(2)}$



## B2. Octal Number System

base = 8

8 symbols : **0,1,2,3,4,5,6** and 7

$$N_{(8)} = d_n 8^n + d_{n-1} 8^{n-1} + d_{n-2} 8^{n-2} + \dots + d_2 8^2 + d_1 8^1 + d_0 8^0$$

**Example 2:** Convert  $1357_{(8)}$  into decimal number.

$$\begin{aligned} 1357_{(8)} &= 1 \times 8^3 + 3 \times 8^2 + 5 \times 8^1 + 7 \times 8^0 \\ &= 512 + 192 + 40 + 7 \\ &= \mathbf{751}_{(10)} \end{aligned}$$

## B2. Octal Number System

### Classwork 2

Convert the following octal numbers to decimal equivalent: (a)  $76543_{(8)}$  ; (b)  $666666_{(8)}$



## B3. Hexadecimal Number System

base = 16

16 symbols : 0,1,2,3,4,5,6,7,8,9,A,B,C,D,E and F

$$N_{(16)} = d_n 16^n + d_{n-1} 16^{n-1} + \dots + d_2 16^2 + d_1 16^1 + d_0 16^0$$

**Example 3:** Convert  $1357_{(16)}$  into decimal number.

$$\begin{aligned} 1357_{(16)} &= 1 \times 16^3 + 3 \times 16^2 + 5 \times 16^1 + 7 \times 16^0 \\ &= 4096 + 768 + 80 + 7 \\ &= \mathbf{4951}_{(10)} \end{aligned}$$

## B3. Hexadecimal Number System

### Classwork 3

Convert the following hexadecimal numbers to decimal:

(a)  $ABCD_{(16)}$  ; (b)  $F4240_{(16)}$



## C. Convert Decimal Number to Other Radix

Divide the decimal integer progressively by the required radix, writing down the *remainder* after each division, the remainders taken in *reverse order* form the number of required radix.

# C1. Convert Decimal to Binary

**Example 4:** Convert  $43_{(10)}$  to binary

$$\begin{array}{r}
 2 \overline{)43} \\
 2 \overline{)21} \text{ ——— } 1 \\
 2 \overline{)10} \text{ ——— } 1 \\
 2 \overline{)5} \text{ ——— } 0 \\
 2 \overline{)2} \text{ ——— } 1 \\
 1 \text{ ——— } 0
 \end{array}$$

$$43_{(10)} = \mathbf{101011}_{(2)}$$

# C1. Convert Decimal to Binary

## Classwork 4

Convert the following decimal numbers to binary:

(a)  $100_{(10)}$ ; (b)  $1357_{(10)}$



## C2. Convert Decimal to Octal

**Example 5:** Convert  $2345_{(10)}$  into octal

$$\begin{array}{r}
 8 \ ) \ 2345 \\
 \hline
 8 \ ) \ 293 \ \underline{\hspace{1cm}} \ 1 \\
 \hline
 8 \ ) \ 36 \ \underline{\hspace{1cm}} \ 5 \\
 \hline
 \phantom{8 \ ) \ } 4 \ \underline{\hspace{1cm}} \ 4
 \end{array}$$

$$2345_{(10)} = \mathbf{4451}_{(8)}$$

## C2. Convert Decimal to Octal

### Classwork 5

Convert the following decimal numbers to octal:

(a)  $63440_{(10)}$  ; (b)  $999999_{(10)}$



## C3. Convert Decimal to Hexadecimal

**Example 6:** Convert  $12345_{(10)}$  into hexadecimal

$$\begin{array}{r}
 16 \overline{) 1234} \\
 16 \overline{) 771} \text{ --- } 9 \\
 16 \overline{) 48} \text{ --- } 3 \\
 \phantom{16} 3 \text{ --- } 0
 \end{array}$$

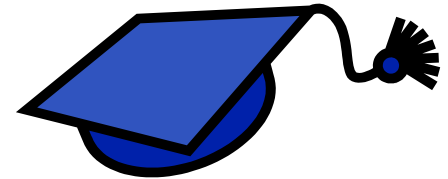
$$12345_{(10)} = \mathbf{3039}_{(16)}$$

## C3. Convert Decimal to Hexadecimal

### Classwork 6

Convert the following decimal numbers to hexadecimal:

(a)  $63440_{(10)}$ ; (b)  $999999_{(10)}$



## D. Binary System

Most digital computers store and manipulate data as **patterns of binary states**. The advantages of binary system over digital system with more than two states are:

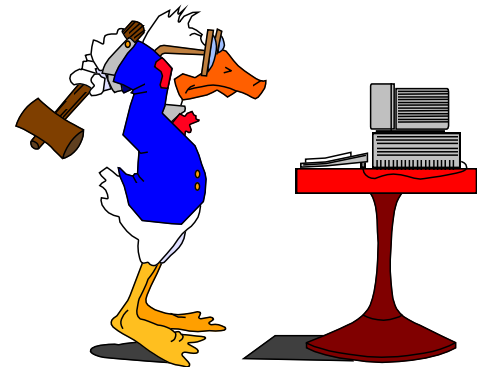
- a. Binary system has only **2 possible states**, either 0 and 1. They can easily be implemented by **physical devices** with **2 stable** states; e.g. a switch with **On/Off** states, a transistor with **Saturation/Cutoff** states.

## D. Binary System

- b. Practically, a **multi-state** device will be **expensive** and **sensitive to noise**. For binary device, a distorted pulse can easily be regenerated and the stability of the two possible states can easily be kept.
- c. **Boolean Algebra** can be applied in binary system design.
- d. Arithmetic and logical operations are simple both in hardware and software construction

## D. Binary System

It should be mentioned that the binary number system is a convenient, but not the only way in manipulating the patterns of binary data. In some applications, such as **programming** and **I/O processes**, the *octal* and *hexadecimal* systems are also adopted because of their simplicity.



## D1. Binary to Octal Conversion

As 8 is the third power of 2, we *group the binary bits in threes*, starting at the **binary point**, then convert each group of three bits to its octal equivalent (**0s are added if necessary**)

Binary	Octal	Binary	Octal
000	0	100	4
001	1	101	5
010	2	110	6
011	3	111	7

# D1. Binary to Octal Conversion

## Example 8

Convert  $110011101_{(2)}$  to octal

$$110011101_{(2)} = \underline{110} \underline{011} \underline{101}_{(2)} = 635_{(8)}$$



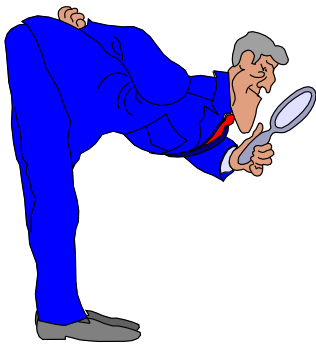
## Classwork 8

Convert the following binary numbers to octal:

(a)  $101010110000_{(2)}$  ; (b)  $1110111011000101_{(2)}$

## D2. Octal to Binary Conversion

It is the reverse of the binary to octal conversion, we merely convert one octal digit at a time to its binary equivalent.



### Example 9

Convert  $456_{(8)}$  to binary

$$456_{(8)} = \underline{100} \underline{101} \underline{110} = 100101110_{(2)}$$

### Classwork 9

Convert the following octal numbers to binary:

(a)  $7654_{(8)}$  ; (b)  $12345_{(8)}$

## D3. Binary to Hexadecimal Conversion

As 16 is the fourth power of 2, we *group the binary bits in fours*, starting at the **binary point**, then convert each group of four bits to its hexadecimal equivalent (**0s are added if necessary**).

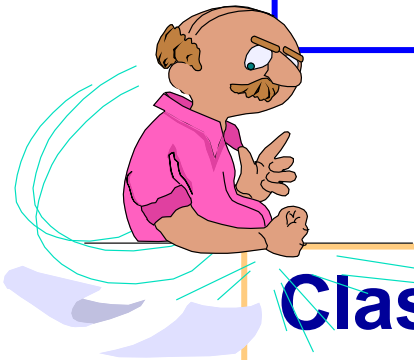
Binary	Hexadecimal	Binary	Hexadecimal
0000	0	1000	8
0001	1	1001	9
0010	2	1010	A
0011	3	1011	B
0100	4	1100	C
0101	5	1101	D
0110	6	1110	E
0111	7	1111	F

## D3. Binary to Hexadecimal Conversion

### Example 10

Convert  $110001101111_{(2)}$  to hexadecimal

$$110001101111_{(2)} = \underline{1100} \underline{0110} \underline{1111} = \mathbf{C6F}_{(16)}$$



### Classwork 10

Convert the following binary numbers to hexadecimal:

(a)  $101010110000_{(2)}$  ; (b)  $110111011000101_{(2)}$

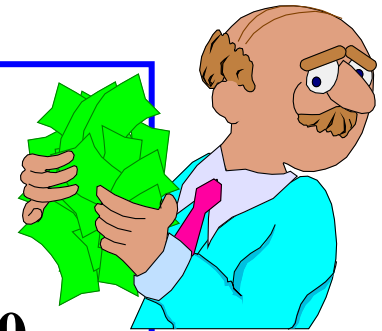
## D4. Hexadecimal to Binary Conversion

It is the reverse of the binary to hexadecimal conversion, we merely convert one hexadecimal digit at a time to its binary equivalent.

### Example 11

Convert  $ABC_{(16)}$  to binary

$$ABC_{(16)} = \underline{1010} \underline{1011} \underline{1100}_{(2)} = 101010111100_{(2)}$$



### Classwork 11

Convert the following hexadecimal numbers to binary:

(a)  $CDEF_{(16)}$  ; (b)  $12345_{(16)}$

# E. Coding Systems

## E1. Computer Words

- ◆ A word stored in a computer's memory can contain either of two kinds of information – *instruction* or *data*
- ◆ Data can be numerical or character information that is processed by a program
- ◆ Instructions are executed by the CPU (Central Processing Unit) to complete special tasks

## E2. Data / Information Representation

There are different types of information to be processed within computer. The most common types are:

- ◆ Numerical value
- ◆ Text
- ◆ Picture

When numbers, letters, or words are represented by a special group of symbols, we say that they are being encoded, and the group of symbols is called a *code*.

## E2. Data / Information Representation

For different types of information, there are different codes to represent them.

Each **binary digit** is known as a bit, and its combinations are:

**4-bit** nibble

**8-bit** byte

**16-bit** word

**32-bit** long word, double word

**64-bit** quad word

## E2.1 Integer

- ◆ Usually coded in *straight binary code*
- ◆ Arbitrary numbers can be represented with the digits 0 and 1, the minus sign (if any)
- ◆ However, in computer, multiple of 8 bits are commonly used
- ◆ *8-bit* represent 0 to 255 for unsigned, -128 to +127 for signed number
- ◆ *16-bit* represent 0 - 65535 for unsigned, -32768 to +32767 for signed number
- ◆ *Sign-magnitude* or *Two's complement* can be used to represent signed number

## E2.2 Binary-Coded-Decimal (BCD)

- ◆ *Straight binary code* is used within digital system, while *decimal code* is used in real world in nature. Hence, conversion between decimal and binary code has to be carried out often
- ◆ *BCD* is used with each digit of a decimal number is represented by its binary equivalent
- ◆ Since a decimal digit can be as large as 9, four bits are required to code each digit

## E2.2 Binary-Coded-Decimal (BCD)

### Example 12

Convert  $928_{10}$  to BCD code.

9  
↓  
1001

2  
↓  
0010

8  
↓  
1000

$928_{10} = 100100101000_{\text{BCD}}$



## E2.2 Binary-Coded-Decimal (BCD)

### Example 13

Convert  $0001\ 0111\ 0011_{\text{BCD}}$  to decimal code.

0001

↓

1

0111

↓

7

0011

↓

3

$000101110011_{\text{BCD}} = 173_{10}$

## E2.3 Alphanumeric Codes

- ◆ Computer must be able to handle non-numerical information.
- ◆ Sometimes refer as *text*, *character*, *character string* or *text string*
- ◆ For every letter, “a” to “z”, upper and lower case, numeric digits, symbols, punctuation marks and control characters, they are represented by a 7-bit code
- ◆ The most common code system used is **ASCII** (**A**merican **S**tandard **C**ode for **I**nformation **I**nterchange)

## E2.3 Alphanumeric Codes

### ASCII Table

Least significant nibble		Most significant nibble							
		0	1	2	3	4	5	6	7
		0000	0001	0010	0011	0100	0101	0110	0111
<b>0</b>	<b>0000</b>	NUL	DLE	SP	0	@	P	'	p
<b>1</b>	<b>0001</b>	SOH	DC1	!	1	A	Q	a	q
<b>2</b>	<b>0010</b>	STX	DC2	"	2	B	R	b	r
<b>3</b>	<b>0011</b>	ETX	DC3	#	3	C	S	c	s
<b>4</b>	<b>0100</b>	EOT	DC4	\$	4	D	T	d	t
<b>5</b>	<b>0101</b>	ENQ	NAK	%	5	E	U	e	u
<b>6</b>	<b>0110</b>	ACK	SYN	&	6	F	V	f	v
<b>7</b>	<b>0111</b>	BEL	ETB	'	7	G	W	g	w
<b>8</b>	<b>1000</b>	BS	CAN	(	8	H	X	h	x
<b>9</b>	<b>1001</b>	HT	EM	)	9	I	Y	i	y
<b>A</b>	<b>1010</b>	LF	SUB	*	:	J	Z	j	z
<b>B</b>	<b>1011</b>	VT	ESC	+	;	K	[	k	{
<b>C</b>	<b>1100</b>	FF	FS	,	<	L	\	l	
<b>D</b>	<b>1101</b>	CR	GS	-	=	M	]	m	}
<b>E</b>	<b>1110</b>	SO	RS	.	>	N	^	n	~
<b>F</b>	<b>1111</b>	SI	US	/	?	O	_	o	DEL

E.g. ASCII code of "A" = **41H** and "a" = **61H**

## E2.4 Parity Method for Error Detection

- ◆ Electrical noise will cause transmission errors when information is sent from a place to another place.
- ◆ Use parity bit is one of the simplest error detection method and most widely used in computer system.
- ◆ Parity bit is an extra bit that is attached to a code group that is being transferred from one location to another. The parity bit is made either 1 or 0.
- ◆ Two different scheme:
  - Even-parity : The value of parity bit is chosen to give an even nos. of 1s in the code group (including the parity bit).

## E2.4 Parity Method for Error Detection

- Even-parity:

1 1000 0011

↑

added parity bit = 1 to make even nos. of 1s, otherwise parity bit = 0

- ◆ Odd-parity : The value of parity bit is chosen to give an odd nos. of 1s in the code group (including the parity bit).

1 1001 0011

↑

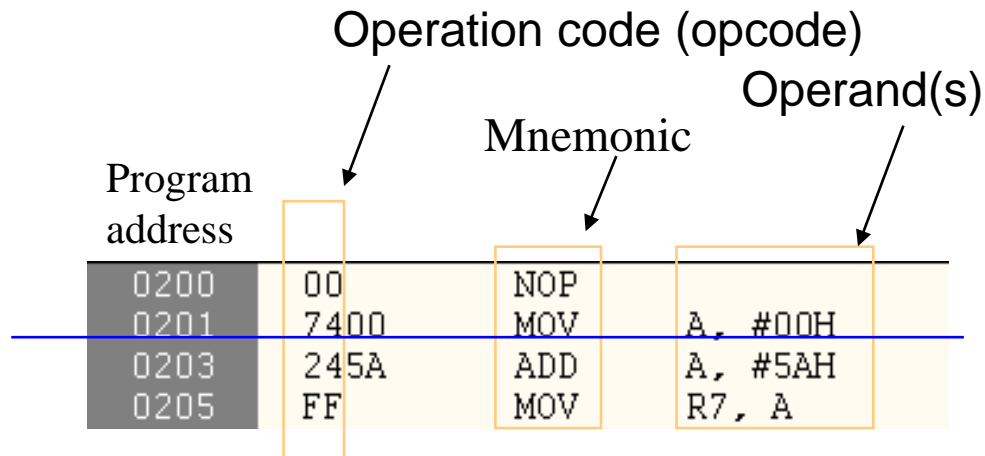
added parity bit = 1 to make odd nos. of 1s, otherwise parity bit = 0

## E2.5 Computer Instructions

- ◆ These are the word which contain the information necessary for a computer to execute its various operations
- ◆ The format and codes for these can vary widely from computer to computer
- ◆ Instruction is consisted of two parts: *opcode* (operation code) and *operand* (data to be operated on)

## E2.5 Computer Instructions

- ◆ *Op code* is used to specify what operation to be executed by the CPU
- ◆ *Operand* is used to specify which/what data to operate on and we can have zero , one and multiple operands
- ◆ Instruction can be of 1-byte, 2-byte and multi-byte in length
- ◆ E.g. For 1-byte long op code, there are  $2^8 = 256$  possible operations to be carried out



### Instruction Set of 8051 Microcontroller

#### Mnemonics, Arranged Alphabetically

MNEMONIC	DESCRIPTION	BYTES	CYCLES	FLAGS
ACALL addr11	PC + 2 → (SP); addr11 → PC	2	2	
ADD A, direct	A + (direct) → A	2	1	C OV AC
ADD A, @Ri	A + (Ri) → A	1	1	C OV AC
ADD A, #data	A + #data → A	2	1	C OV AC
ADD A, Rn	A + Rn → A	1	1	C OV AC
ADDC A, direct	A + (direct) + C → A	2	1	C OV AC
ADDC A, @Ri	A + (Ri) + C → A	1	1	C OV AC
ADDC A, #data	A + #data + C → A	2	1	C OV AC
ADDC A, Rn	A + Rn + C → A	1	1	C OV AC
AJMP addr11	Addr11 → PC	2	2	
ANL A, direct	A AND (direct) → A	2	1	
ANL A, @Ri	A AND (Ri) → A	1	1	

## E2.5 Computer Instructions

### Classwork 16

1. What information is contained in the first byte of a multi-byte instruction?
2. How many possible operations a CPU can have if it used 2 bytes for the op code?

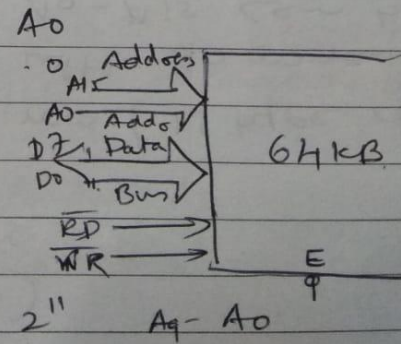
Bit  $\rightarrow$  binary digit that have value 0 or 1  
 Byte  $\rightarrow$  8 bit  
 Nibble  $\rightarrow$  4 bit  
 word 2 byte - 16 bits.

Bit	0			
Nibble	0000			
Byte	0000	0000		
word	0000	0000	0000	0000
	0	0	0	0
	F	F	F	F

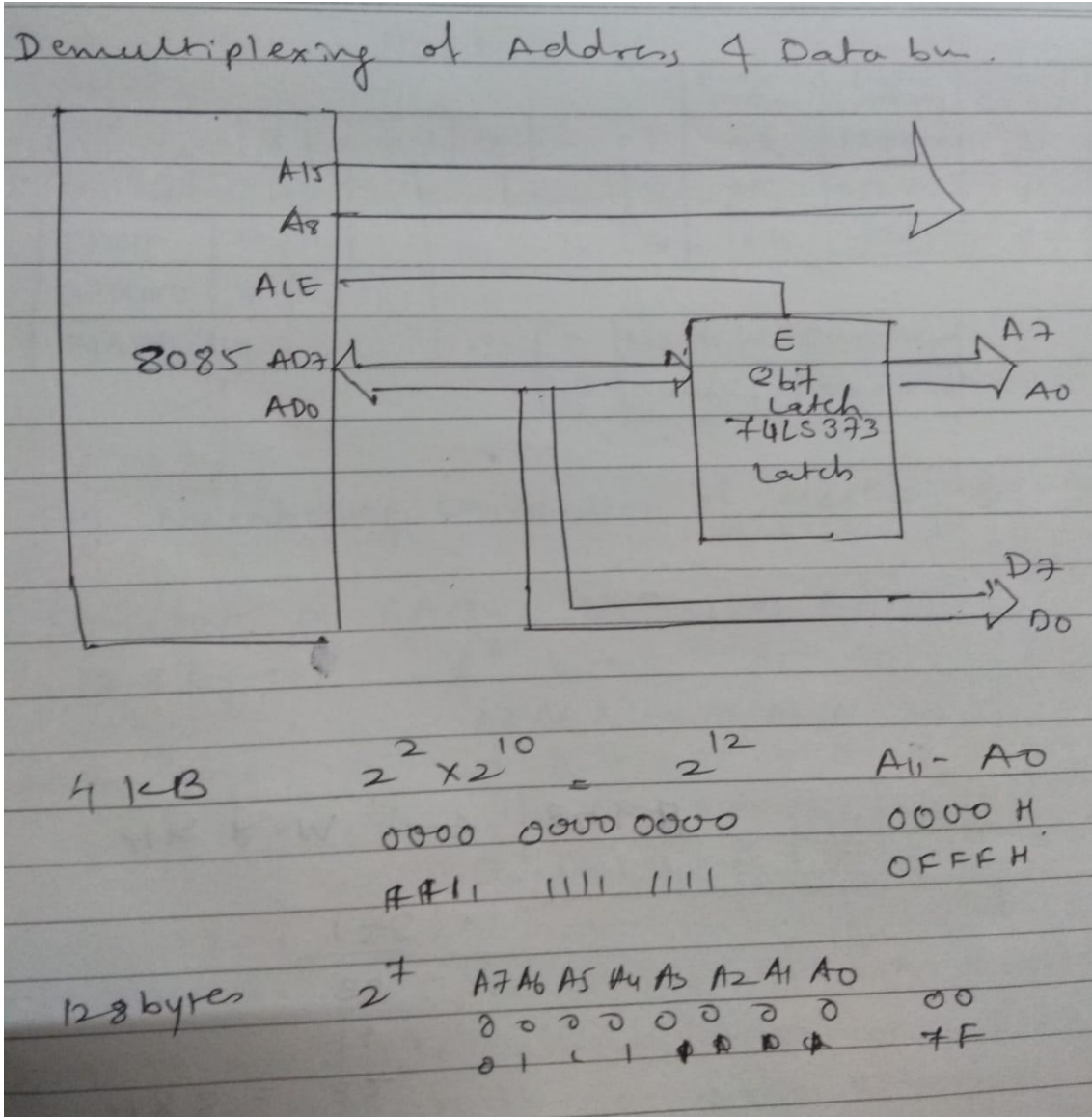
1KB -  $2^{10}$  bytes = 1024 bytes.

eg. 2KB      64KB       $2^6 \times 2^{10} = 2^{16}$   
 4KB                              65,536 m.L

1 megabyte =  $2^{20}$  byte  
 1 GB =  $2^{40}$  bytes



$2KB = 2^1 \times 2^{10} = 2^{11}$       A9 - A0



## Read reference

- ◆ The 8051 Microcontroller and Embedded Systems - Using Assembly and C, Mazidi
  - Chapter 0 P.1 – P.6